EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	259	multiple adj bit adj per adj cell	US-PGPUB; USPAT	OR	ON	2006/06/07 09:06
L2	76	mbpc	US-PGPUB; USPAT	OR	ON	2006/06/07 09:06
L3	333	1 or 2	US-PGPUB; USPAT	OR	ON	2006/06/07 09:07
L4	40	wong-sau-ching.in.	US-PGPUB; USPAT	OR	ON	2006/06/07 09:07

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = WONG
First Name = SAU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09518608	6662263	150	03/03/2000	SECTORLESS FLASH MEMORY ARCHITECTURE	WONG, SAU C.
09670764	6314025	150	09/27/2000	High data rate write process for non-volatile flash memories	WONG, SAU C.
09680797	6307776	150	10/06/2000	Multi-bit-per-cell flash EEPROM memory with refresh	WONG, SAU C.
09768922	6330185	150	01/23/2001	High bandwidth multi-level flash memory using dummy memory accesses to improve precision when writing or reading a data stream	WONG, SAU C.
09967513	6456529	150	09/27/2001	PROGRAMMABLE IMPEDANCE DEVICE	WONG, SAU C.
10042504	6606267	150	10/18/2001	HIGH DATA RATE WRITE PROCESS FOR NON-VOLATILE FLESH MEMORIES	WONG, SAU C.
10045505	6898117	150	10/18/2001	MULTI-BIT-PER-CELL FLASH EEPROM MEMORY WITH REFRESH	WONG, SAU C.
10109353	6556465	150	03/27/2002	ADJUSTABLE CIRCUITS FOR ANALOG OR MULTI-LEVEL MEMORY	WONG, SAU C.
10264970	6624773	150	10/04/2002	DATA ENCRYPTION AND SIGNAL SCRAMBLING USING PROGRAMMABLE DATA CONVERSION ARRAYS	WONG, SAU C.
10359476	Not Issued	93	02/05/2003	INTEGRATED CIRCUIT WITH ANALOG OR MULTILEVEL STORAGE CELLS AND USER-SELECTABLE SAMPLING FREQUENCY	WONG, SAU C.
10382333	6760262	150	03/04/2003	ADJUSTABLE CHARGE PUMP	WONG, SAU C.

				CIRCUIT ADJUSTABLE IN RESPONSE TO AN EXTERNAL VOLTAGE SOURCE	
10461851	6944058	150	06/13/2003	HIGH DATA RATE WRITE PROCESS FOR NON-VOLATILE FLASH MEMORIES	WONG, SAU C.
10678885	Not Issued	95	10/02/2003	FILE STORAGE AND ERASURE IN FLASH MEMORY	WONG, SAU C.
11101938	Not Issued	61	04/07/2005	Multi-bit-per-cell flash EEPROM memory with refresh	WONG, SAU C.
11184322	Not Issued	30	07/18/2005	High data rate write process for non-volatile flash memories	WONG, SAU C.
08333381	5694356	150	11/02/1994	HIGH RESOLUTION ANALOG STORAGE EPROM AND FLASH EPROM	WONG, SAU C.
08535441	5745409	150	09/28/1995	NON-VOLATILE MEMORY WITH ANALOG AND DIGITAL INTERFACE AND STORAGE	WONG, SAU C.
08583905	5687115	150	01/11/1996	WRITE CIRCUITS FOR ANALOG MEMORY FOR FLOATING GATE EEPROM MEMORY	WONG, SAU C.
08585072	5751635	150	01/11/1996	READ CIRCUITS FOR ANALOG MEMORY CELLS	WONG, SAU C.
08585138	5638320	150	01/11/1996	HIGH RESOLUTION ANALOG STORAGE EPROM AND FLASH EPROM	WONG, SAU C.
08587310	5680341	150	01/16/1996	PIPELINED RECORD AND PLAYBACK FOR ANALOG NON-VOLATILE MEMORY	WONG, SAU C.
08598485	5682352	150	02/08/1996	DIGITAL TESTING OF ANALOG MEMORY DEVICES	WONG, SAU C.
08622332	5748533	150	03/26/1996	READ CIRCUIT WHICH USES A COARSE-TO-FINE SEARCH WHEN READING THE THRESHOLD VOLTAGE OF A MEMORY CELL	WONG, SAU C.
08622333	5748534	150	03/26/1996	FEEDBACK LOOP FOR READING THRESHOLD VOLTAGE	WONG, SAU C.
08684788	5818757	150	07/22/1996	ANALOG AND MULTI-LEVEL MEMORY WITH REDUCED PROGRAM DISTURB	WONG, SAU C.

08781741	5923585	150	01/10/1997	SOURCE BIASING IN NON-VOLATILE MEMORY HAVING ROW-BASED SECTORS	WONG, SAU C.
08839288	5949716	150	04/16/1997	LOOK-AHEAD ERASE FOR SEQUENTIAL DATA STORAGE	WONG, SAU C.
08847234	5815425	150	04/30/1997	COMBINED DIGITAL WRITE AND ANALOG REWRITE PROCESS FOR NON-VOLATILE MEMORY	WONG, SAU C.
08847236	5801980	150	05/01/1997	NON-VOLATILE MEMORY WITH ANALOG AND DIGITAL INTERFACE AND STORAGE	WONG, SAU C.
08867473	5909387	150	06/02/1997	MEMORY ARCHITECTURE FOR RECORDING OF MULTIPLE MESSAGES	WONG, SAU C.
08889111	5896340	150	07/07/1997	MULTIPLE ARRAY ARCHITECTURE FOR ANALOG OR MULTI-BIT-PER-CELL MEMORY	WONG, SAU C.
08924909	5909449	150	09/08/1997	MULTIBIT-PER-CELL NON-VOLATILE MEMORY WITH ERROR DETECTION AND CORRECTION	WONG, SAU C.
09053716	6038166	150	04/01/1998	HIGH RESOLUTION MULTI-BIT-PER-CELL MEMORY	WONG, SAU C.
09086785	6594036	150	05/28/1998	ANALOG/MULTI-LEVEL MEMORY FOR DIGITAL IMAGING	WONG, SAU C.
09103623	5969986	150	06/23/1998	HIGH-BANDWIDTH READ AND WRITE ARCHITECTURES FOR NON-VOLATILE MEMORIES	WONG, SAU C.
09128225	6134145	150	08/03/1998	HIGH DATA RATE WRITE PROCESS FOR NON-VOLATILE FLASH MEMORIES	WONG, SAU C.
09159397	Not Issued	164	09/23/1998	ANALOG BUFFER MEMORY FOR HIGH-SPEED DIGITAL IMAGE CAPTURE	WONG, SAU C.
09159704	6169503	150	09/23/1998	PROGRAMMABLE ARRAYS FOR DATA CONVERSIONS BETWEEN ANALOG AND DIGITAL	WONG, SAU C.
09159848	6201734	150	09/25/1998	PROGRAMMABLE IMPEDANCE DEVICE	WONG, SAU C.

09199971	6345000	150	11/25/1998	FLASH MEMORY PERMITTING SIMULTANEOUS READ/WRITE AND ERASE OPERATIONS IN A SINGLE MEMORY ARRAY	WONG, SAU C.
09200205	6154157	150	11/25/1998	NON-LINEAR MAPPING OF THRESHOLD VOLTAGES FOR ANALOG/MULTI-LEVEL MEMORY	WONG, SAU C.
09200220	6151246	150	11/25/1998	MULTI-BIT-PER-CELL FLASH EEPROM MEMORY WITH REFRESH	WONG, SAU C.
09224168	6760068	150	12/31/1998	CORRECTION OF CORRUPTED ELEMENTS IN SENSORS USING ANALOG/MULTI-LEVEL NON-VOLATILE MEMORY	WONG, SAU C.
09224183	6058060	150	12/31/1998	MULTI-BIT-PER-CELL AND ANALOG/MULTI-LEVEL NON-VOLATILE MEMORIES WITH IMPROVED RESOLUTION AD SIGNAL-TO NOISE RATIO	WONG, SAU C.
09293133	6160739	150	04/16/1999	NON-VOLATILE MEMORIES WITH IMPROVED ENDURANCE AND EXTENDED LIFETIME	WONG, SAU C.
09305512	Not Issued	161	05/05/1999	MULTI-BIT-PER-CELL AND ANALOG/MULTI-LEVEL NON-VOLATILE MEMORIES WITH IMPROVED RESOLUTION AND SIGNAL-TO-NOISE RATIO	WONG, SAU C.
09305544	6285593	150	05/05/1999	WORD-LINE DECODER FOR MULTI-BIT-PER-CELL AND ANALOG/MULTI-LEVEL MEMORIES WITH IMPROVED RESOLUTION AND SIGNAL-TO-NOISE RATIO	WONG, SAU C.
09324902	6208542	150	06/02/1999	TECHNIQUES FOR STORING DIGITAL DATA IN AN ANALOG OR MULTILEVEL MEMORY	WONG, SAU C.
09342900	6184726	150	06/29/1999	ADJUSTABLE LEVEL SHIFTER CIRCUITS FOR ANALOG OR MULTILEVEL MEMORIES	WONG, SAU C.
09343117	6549456	150	06/29/1999	INTEGRATED CIRCUIT WITH ANALOG OR MULTILEVEL STORAGE CELLS AND USER-	WONG, SAU C.

| | | | | SELECTABLE SAMPLING FREQUENCY | | |

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wong	sau	Search

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